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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

OCT 14 2005

In re: Application of: Madhav Datta

Group Art Unit: 2829

Serial No.: 10/688,418

Examiner: Karlsen, Ernest F.

Filed: Oct. 17, 2003

In Response to Office Action  
Dated: July 14, 2005

For: Method for Testing Chips on Flat Solder Bumps

Attorney Docket No.: 64,610-042A (YO998-431US2)

CERTIFICATE OF MAILING OR FACSIMILE TRANSMISSION

I hereby certify that this correspondence is (1)  being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P O Box 1450, Alexandria, VA 22313-1450 on October 14, 2005; or (2)  being facsimile transmitted to the United States Patent and Trademark Office at facsimile number (571)-273-8300 on October 14, 2005.

Randy W. Tung  
Printed Name

  
Signature

10/14/05  
Date

Please forward all correspondence to  
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838 W. Long Lake Road, Suite 120  
Bloomfield Hills, MI 48302

REQUEST FOR RECONSIDERATION

Commissioner for Patents  
Alexandria, VA 22313-1450

Dear Sir:

In response to an Office Action mailed July 14, 2005, please consider the following remarks.